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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Lin, *et al.* Docket No.: TSM03-0670  
Serial No.: 10/729,092 Art Unit: 2815  
Filed: December 5, 2003 Examiner: Matthew E. Warren  
For: Structure and Method of Forming Integrated Circuits Utilizing Strained Channel Transistors

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Date of Deposit: October 8, 2004

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Information Disclosure Statement (1 page)  
IDS Form PTO/SB/08a and 08b (2 pages) citing (15) references  
Copies of (5) cited references  
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Respectfully submitted,

*Natalie Swider*

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Mail Stop: Amendment  
Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

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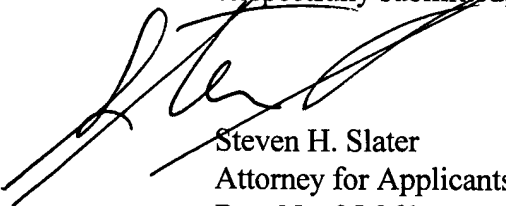
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Respectfully submitted,

October 8, 2004

Date

  
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U.S. PATENT DOCUMENTS

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Examiner  
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**Complete if Known**

Application Number	10/729.092
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<b>Filing Date</b>	<b>December 5, 2003</b>
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First Named Inventor	Lin, et al.
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Art Unit	2815
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Examiner Name	TBD
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Sheet

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Attorney Docket Number	TSM03-0670
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## NON PATENT LITERATURE DOCUMENTS

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Examiner Signature		Date Considered	
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